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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Pan et al.

Art Unit: 2815

Serial No.: 10/669,111

Examiner: Lee, E.

Filing Date: 09/23/2003

Docket No.: TI-36040

Customer No.: 23494

Conf. No.: 3098

Title: DEPLETION DRAIN-EXTENDED MOS TRANSISTORS AND
METHODS FOR MAKING THE SAME

ELECTION

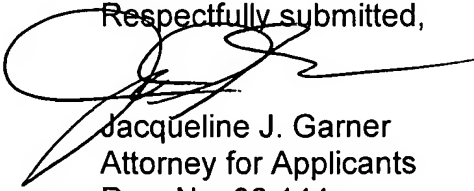
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<p>MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A) I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as first Class Mail in an envelope addressed to Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450.</p> <p><u>9-10-04</u> Date <u>Marianna Smith</u> Marianna Smith</p>

Dear Sir:

With respect to the Restriction Requirement mailed on 08/11/2004, the Examiner has restricted the instant application to the invention of Group I (Claims 19-26), or Group II (Claims 1-18). In light of this, Applicants elect to pursue Group II, (Claims 1-18) without traverse.

Respectfully submitted,


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